

DATA SHEET

NE/SA/SE5534/5534A

Single low noise operational amplifier

Product data
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2001 Aug 03

Single low noise operational amplifier

NE/SA/SE5534/5534A

DESCRIPTION

The NE/SA/SE5534/5534A are single high-performance low noise operational amplifiers. Compared to other operational amplifiers, such as TL083, they show better noise performance, improved output drive capability, and considerably higher small-signal and power bandwidths.

This makes the devices especially suitable for application in high quality and professional audio equipment, in instrumentation and control circuits and telephone channel amplifiers. The op amps are internally compensated for gain equal to, or higher than, three. The frequency response can be optimized with an external compensation capacitor for various applications (unity gain amplifier, capacitive load, slew rate, low overshoot, etc.)

FEATURES

- Small-signal bandwidth: 10 MHz
- Output drive capability: 600 Ω , 10V_{RMS} at V_S = ± 18 V
- Input noise voltage: 4nV / $\sqrt{\text{Hz}}$
- DC voltage gain: 100000
- AC voltage gain: 6000 at 10 kHz
- Power bandwidth: 200 kHz
- Slew rate: 13 V/ μ s
- Large supply voltage range: ± 3 to ± 20 V

PIN CONFIGURATIONS

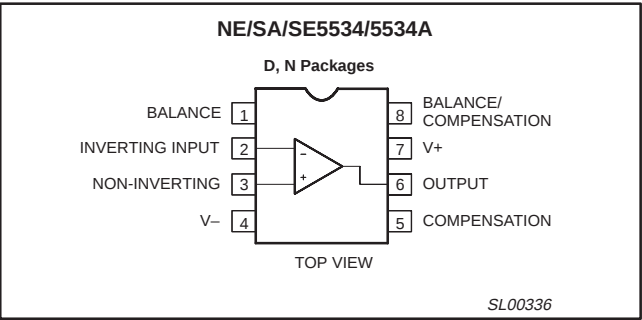


Figure 1. Pin Configuration

APPLICATIONS

- Audio equipment
- Instrumentation and control circuits
- Telephone channel amplifiers
- Medical equipment

ORDERING INFORMATION

DESCRIPTION	TEMPERATURE RANGE	ORDER CODE	DWG #
8-Pin Plastic Small Outline (SO) package	0 °C to +70 °C	NE5534D	SOT96-1
8-Pin Plastic Dual In-Line Package (DIP)	0 °C to +70 °C	NE5534N	SOT97-1
8-Pin Plastic Small Outline (SO) package	0 °C to +70 °C	NE5534AD	SOT96-1
8-Pin Plastic Dual In-Line Package (DIP)	0 °C to +70 °C	NE5534AN	SOT97-1
8-Pin Plastic Dual In-Line Package (DIP)	-40 °C to +85 °C	SA5534N	SOT97-1
8-Pin Plastic Small Outline (SO) package	-40 °C to +85 °C	SA5534AD	SOT96-1
8-Pin Plastic Dual In-Line Package (DIP)	-40 °C to +85 °C	SA5534AN	SOT97-1
8-Pin Plastic Dual In-Line Package (DIP)	-55 °C to +125 °C	SE5534N	SOT97-1
8-Pin Plastic Dual In-Line Package (DIP)	-55 °C to +125 °C	SE5534AN	SOT97-1

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ABSOLUTE MAXIMUM RATINGS

SYMBOL	PARAMETER	RATING	UNIT
V_S	Supply voltage	± 22	V
V_{IN}	Input voltage	$\pm V$ supply	V
V_{DIFF}	Differential input voltage ¹	± 0.5	V
T_{amb}	Operating temperature range NE SA SE	0 to +70 –40 to +85 –55 to +125	°C °C °C
T_{stg}	Storage temperature range	–65 to +150	°C
T_j	Junction temperature	150	°C
P_D	Power dissipation at 25 °C ² SO8 package DIP8 package	750 1150	mW mW
	Output short-circuit duration ³	Indefinite	
T_{sld}	Lead soldering temperature (10 sec max)	230	°C

NOTES:

- Diodes protect the inputs against over voltage. Therefore, unless current-limiting resistors are used, large currents will flow if the differential input voltage exceeds 0.6 V. Maximum current should be limited to ± 10 mA.
- For operation at elevated temperature, derate packages based on the following junction-to-ambient thermal resistance:
8-pin plastic DIP 105 °C/W
8-pin plastic SO 160 °C/W
- Output may be shorted to ground at $V_S = \pm 15$ V, $T_{amb} = 25$ °C. Temperature and/or supply voltages must be limited to ensure dissipation rating is not exceeded.

DC ELECTRICAL CHARACTERISTICS

$T_{amb} = 25$ °C; $V_S = \pm 15$ V, unless otherwise specified. 1, 2, 3

SYMBOL	PARAMETER	TEST CONDITIONS	NE/SA5534/5534A			SE5534/5534A			UNIT
			Min	Typ	Max	Min	Typ	Max	
V_{OS}	Offset voltage	Over temperature		0.5	4		0.5	2	mV
$\Delta V_{OS}/\Delta T$				5	5		5	3	mV/°C
I_{OS}	Offset current	Over temperature		20	300		10	200	nA
$\Delta I_{OS}/\Delta T$				200	400		200	500	nA/°C
I_B	Input current	Over temperature		500	1500		400	800	nA
$\Delta I_B/\Delta T$				5	2000		5	1500	nA/°C
I_{CC}	Supply current per op amp	Over temperature		4	8		4	6.5	mA
					10			9	mA
V_{CM}	Common mode input range		± 12	± 13		± 12	± 13		V
CMRR	Common mode rejection ratio		70	100		80	100		dB
PSRR	Power supply rejection ratio			10	100		10	50	$\mu V/V$
A_{VOL}	Large-signal voltage gain	$R_L \geq 600 \Omega$, $V_O = \pm 10$ V	25	100		50	100		V/mV
		Over temperature	15			25			V/mV
V_{OUT}	Output swing	$R_L \geq 600 \Omega$	± 12	± 13		± 12	± 13		V
		Over temperature	± 10	± 12		± 10	± 12		V
		$R_L \geq 600 \Omega$; $V_S = \pm 18$ V	± 15	± 16		± 15	± 16		V
		$R_L \geq 2 k\Omega$	± 13	± 13.5		± 13	± 13.5		V
		Over temperature	± 12	± 12.5		± 12	± 12.5		V
R_{IN}	Input resistance		30	100		50	100		k Ω
I_{SC}	Output short circuit current			38			38		mA

NOTES:

- For NE5534/5534A, $T_{MIN} = 0$ °C, $T_{MAX} = 70$ °C
- For SA5534/5534A, $T_{MIN} = -40$ °C, $T_{MAX} = +85$ °C
- For SE5534/5534A, $T_{MIN} = -55$ °C, $T_{MAX} = +125$ °C

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AC ELECTRICAL CHARACTERISTICS

$T_{amb} = 25\text{ }^{\circ}\text{C}$, $V_S = \pm 15\text{ V}$, unless otherwise specified.

SYMBOL	PARAMETER	TEST CONDITIONS	NE/SA5534/5534A			SE5534/5534A			UNIT
			Min	Typ	Max	Min	Typ	Max	
R_{OUT}	Output resistance	$A_V = 30\text{ dB}$ closed-loop $f = 10\text{ kHz}$; $R_L = 600\text{ }\Omega$; $C_C = 22\text{ pF}$		0.3			0.3		Ω
	Transient response	Voltage-follower, $V_{IN} = 50\text{ mV}$ $R_L = 600\text{ }\Omega$; $C_C = 22\text{ pF}$, $C_L = 100\text{ pF}$							
t_R	Rise time			20			20		ns
	Overshoot			20			20		%
	Transient response	$V_{IN} = 50\text{ mV}$, $R_L = 600\text{ }\Omega$ $C_C = 47\text{ pF}$, $C_L = 500\text{ pF}$							
t_R	Rise time			50			50		ns
	Overshoot			35			35		%
A_V	Gain	$f = 10\text{ kHz}$, $C_C = 0$ $f = 10\text{ kHz}$, $C_C = 22\text{ pF}$		6 2.2			6 2.2		V/mV V/mV
GBW	Gain bandwidth product	$C_C = 22\text{ pF}$, $C_L = 100\text{ pF}$		10			10		MHz
SR	Slew rate	$C_C = 0$ $C_C = 22\text{ pF}$		13 6			13 6		V/ μs V/ μs
	Power bandwidth	$V_{OUT} = \pm 10\text{ V}$, $C_C = 0\text{ pF}$ $V_{OUT} = \pm 10\text{ V}$, $C_C = 22\text{ pF}$ $V_{OUT} = \pm 14\text{ V}$, $R_L = 600\text{ }\Omega$ $C_C = 22\text{ pF}$, $V_{CC} = \pm 18\text{ V}$		200 95 70			200 95 70		kHz kHz kHz

ELECTRICAL CHARACTERISTICS

$T_{amb} = 25\text{ }^{\circ}\text{C}$, $V_S = 15\text{ V}$, unless otherwise specified.

SYMBOL	PARAMETER	TEST CONDITIONS	NE/SA/SE5534			NE/SA/SE5534A			UNIT
			Min	Typ	Max	Min	Typ	Max	
V_{NOISE}	Input noise voltage	$f_O = 30\text{ Hz}$ $f_O = 1\text{ kHz}$		7 4			5.5 3.5	7 4.5	nV/ $\sqrt{\text{Hz}}$ nV/ $\sqrt{\text{Hz}}$
I_{NOISE}	Input noise current	$f_O = 30\text{ Hz}$ $f_O = 1\text{ kHz}$		2.5 0.6			1.5 0.4		pA/ $\sqrt{\text{Hz}}$ pA/ $\sqrt{\text{Hz}}$
	Broadband noise figure	$f = 10\text{ Hz}$ to 20 kHz ; $R_S = 5\text{ k}\Omega$					0.9		dB
	Channel separation	$f = 1\text{ kHz}$; $R_S = 5\text{ k}\Omega$		110			110		dB

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EQUIVALENT SCHEMATIC

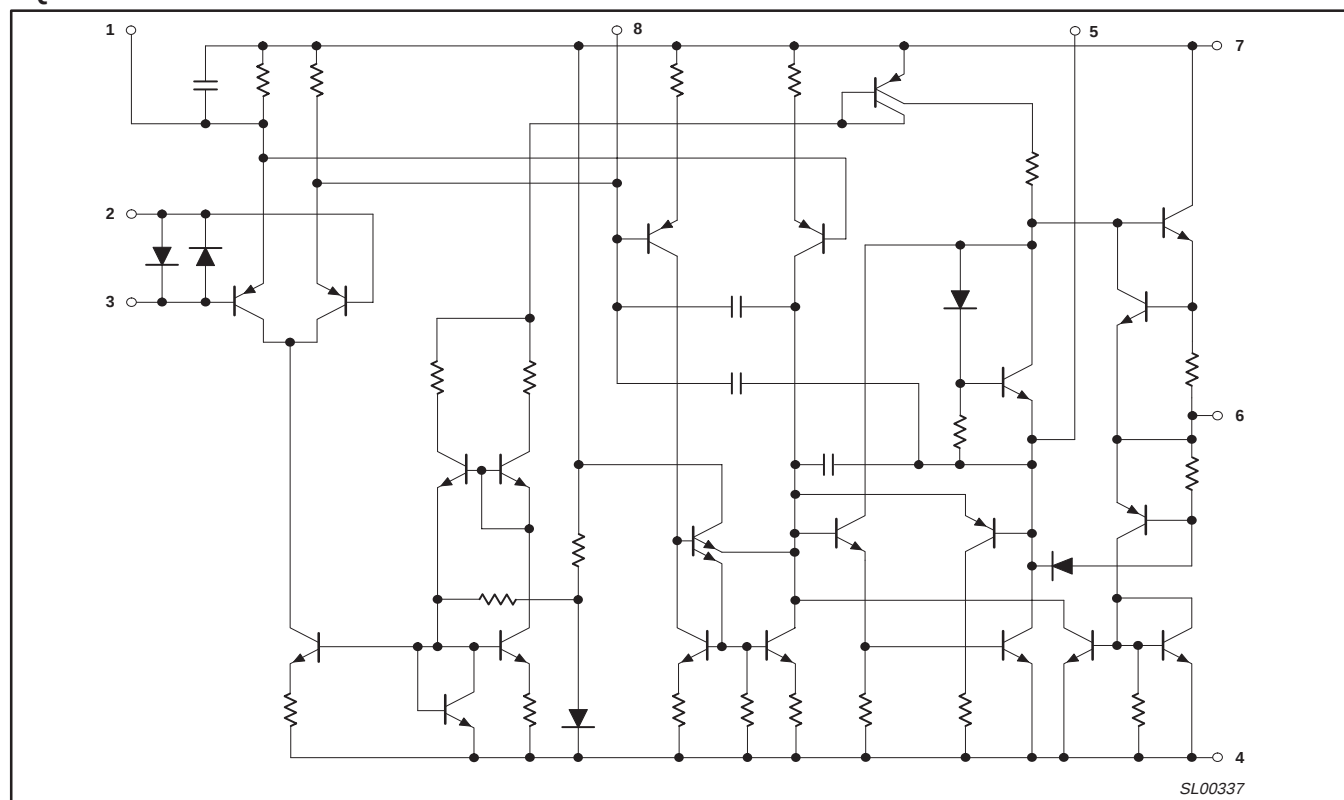
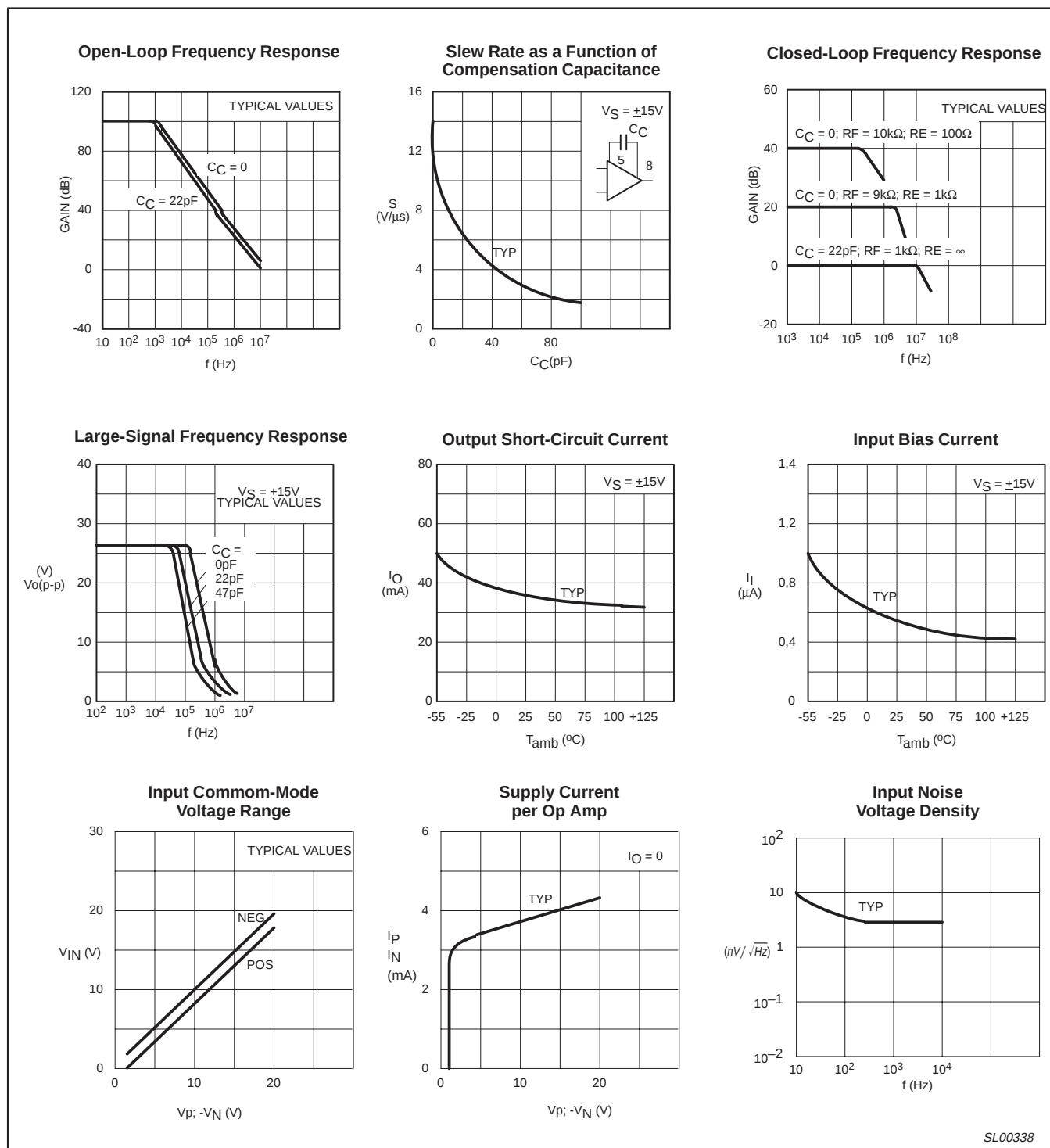


Figure 2. Equivalent Schematic

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TYPICAL PERFORMANCE CHARACTERISTICS



SL00338

Figure 3. Typical Performance Characteristics

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TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

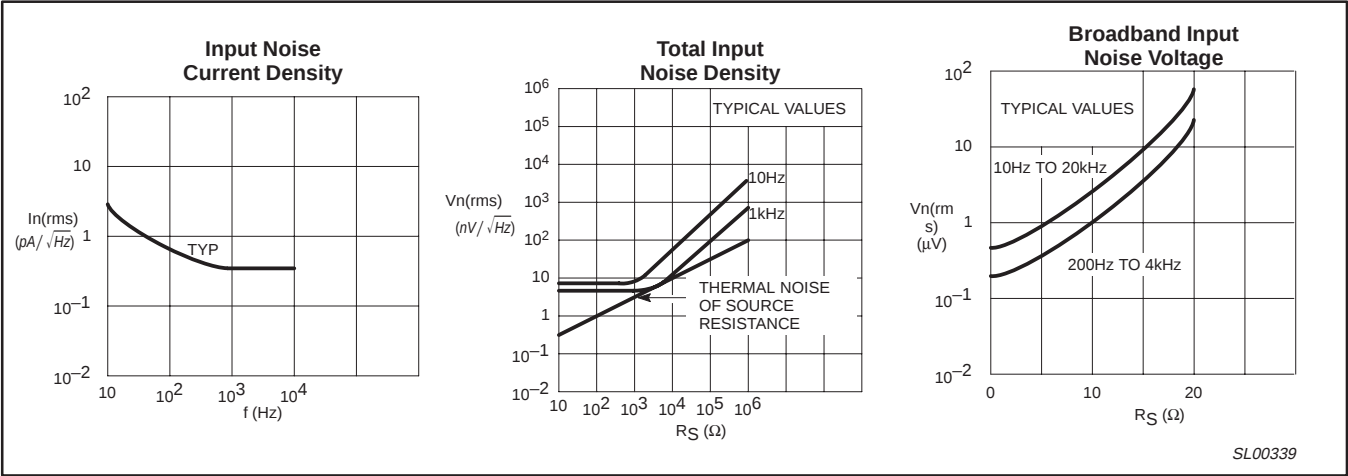


Figure 4. Typical Performance Characteristics (cont.)

TEST LOAD CIRCUITS

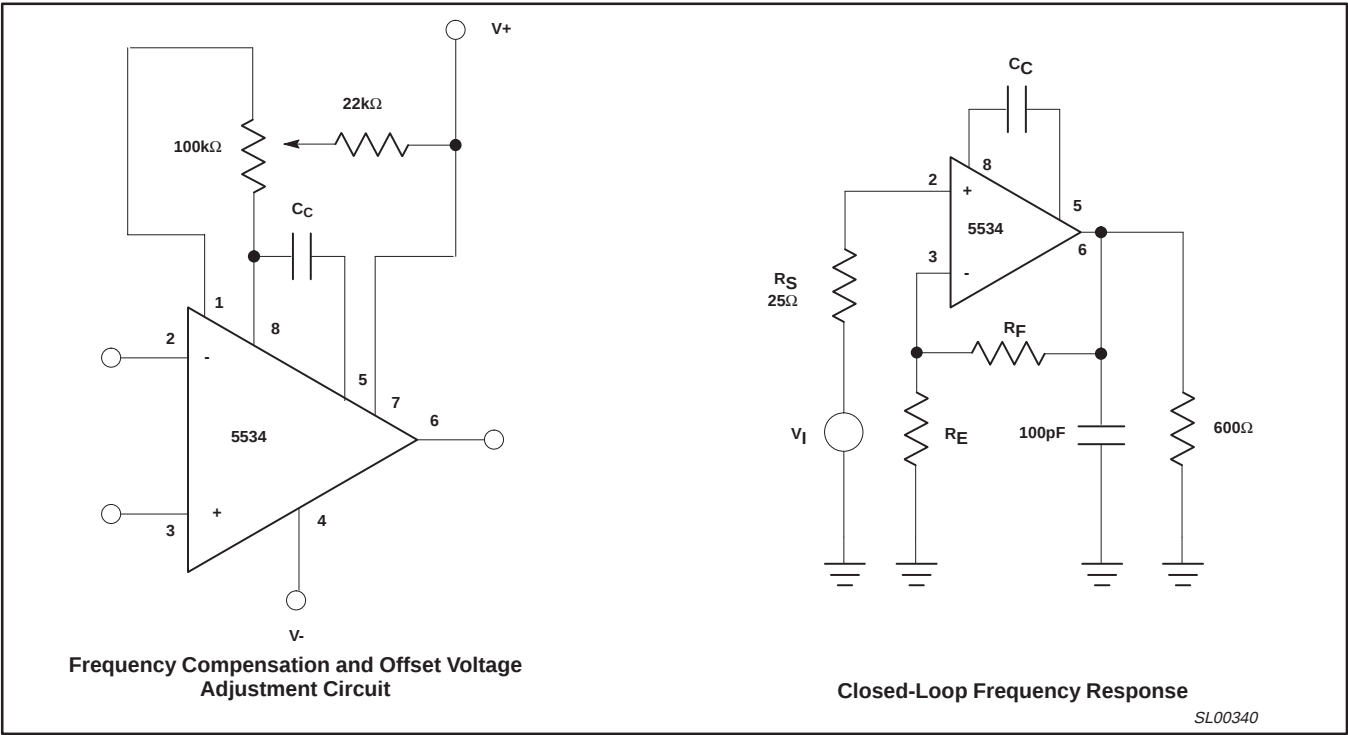


Figure 5. Test Load Circuits

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NOISE TEST BLOCK DIAGRAM

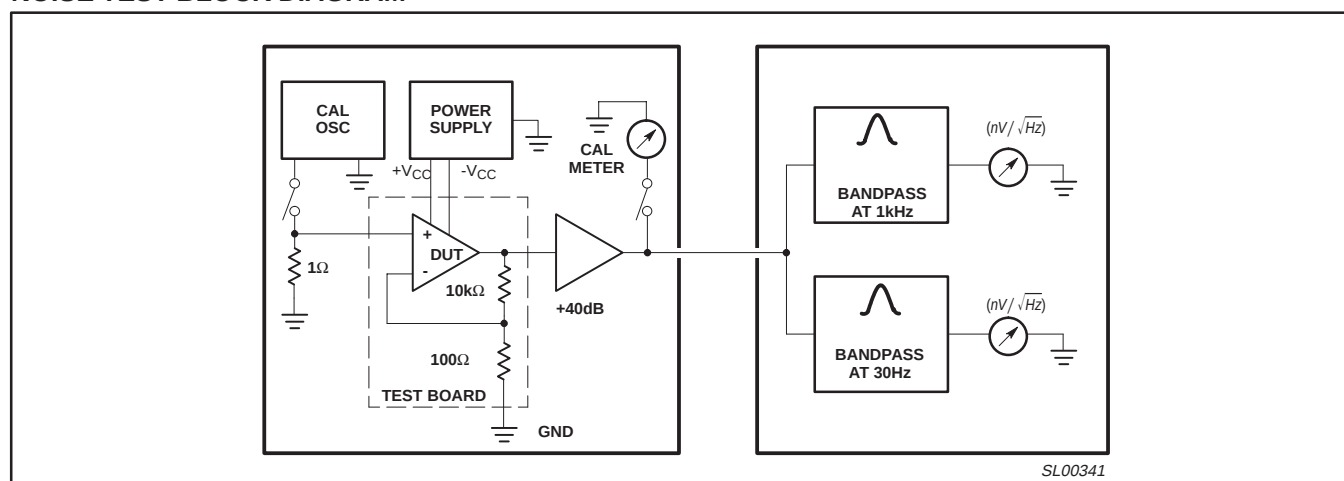


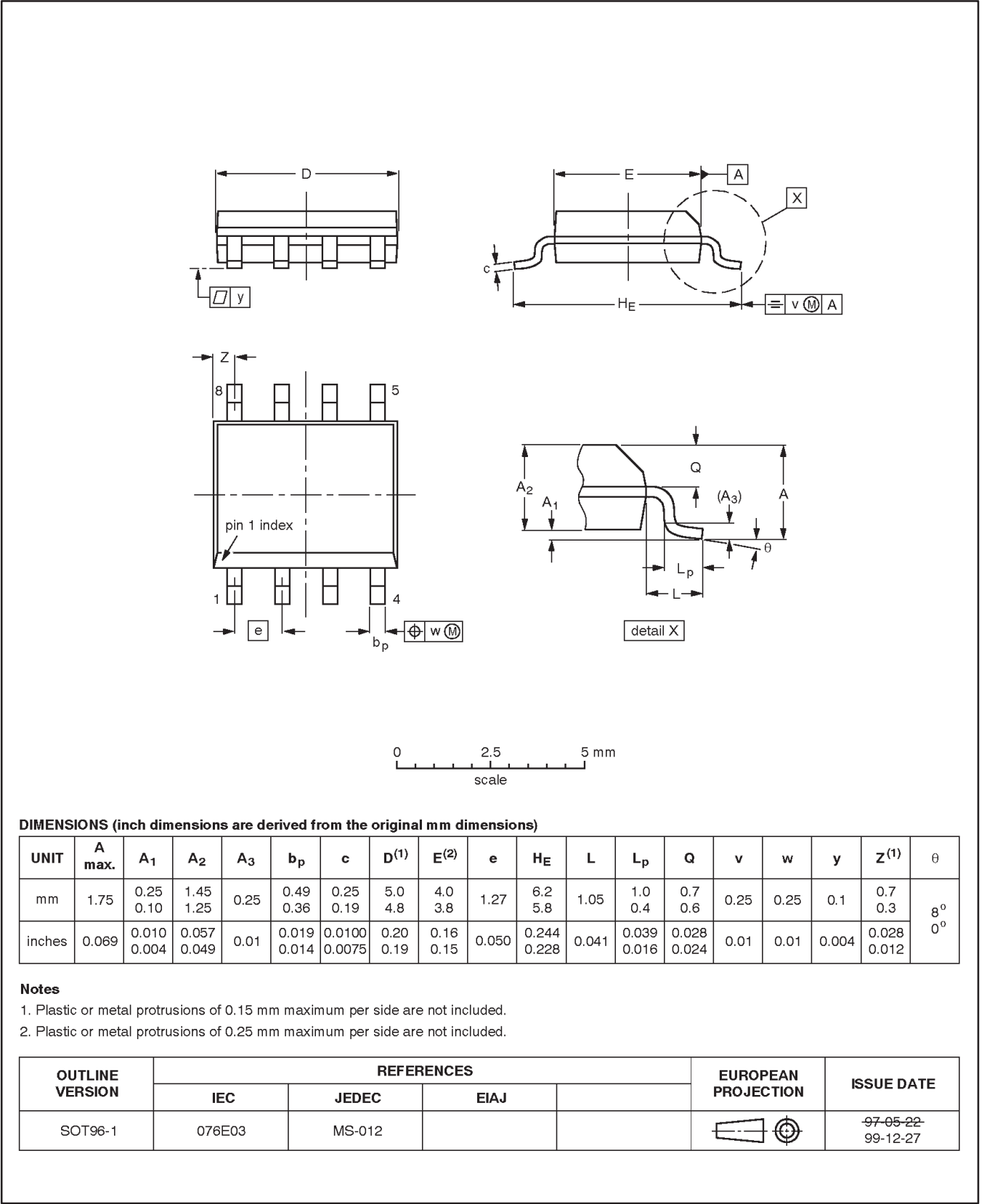
Figure 6. Noise Test Block Diagram

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SO8: plastic small outline package; 8 leads; body width 3.9 mm

SOT96-1



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DIP8: plastic dual in-line package; 8 leads (300 mil)

SOT97-1

Technical drawing of the SOT97-1 package showing top, side, and end views with dimensions A, A1, A2, b, b1, b2, c, D, E, L, ME, MH, w, Z, e, and pin 1 index.

0 5 10 mm

scale

DIMENSIONS (inch dimensions are derived from the original mm dimensions)

UNIT	A max.	A ₁ min.	A ₂ max.	b	b ₁	b ₂	c	D ⁽¹⁾	E ⁽¹⁾	e	e ₁	L	M _E	M _H	w	Z ⁽¹⁾ max.
mm	4.2	0.51	3.2	1.73 1.14	0.53 0.38	1.07 0.89	0.36 0.23	9.8 9.2	6.48 6.20	2.54	7.62	3.60 3.05	8.25 7.80	10.0 8.3	0.254	1.15
inches	0.17	0.020	0.13	0.068 0.045	0.021 0.015	0.042 0.035	0.014 0.009	0.39 0.36	0.26 0.24	0.10	0.30	0.14 0.12	0.32 0.31	0.39 0.33	0.01	0.045

Note

1. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT97-1	050G01	MO-001	SC-504-8			95-02-04 99-12-27

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NOTES

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Data sheet status

Data sheet status ^[1]	Product status ^[2]	Definitions
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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